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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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| 10/619,033 | 07/10/2003 | Lixin Zhao | | 6392 |
| 7590 | 08/31/2004 | | EXAMINER | |
| Lixin Zhao 43454 Bryant St. Fremont, CA 94539 | | | | BREWSTER, WILLIAM M |
| | | | ART UNIT | PAPER NUMBER |
| | | | 2823 | |

DATE MAILED: 08/31/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | | |
|------------------------------|---------------------------------|---------------------|--|
| Office Action Summary | Application No. | Applicant(s) | |
| | 10/619,033 | ZHAO, LIXIN | |
| | Examiner William M. Brewster | Art Unit 2823 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 04 August 2004.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 16-35 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 16-18 and 23-35 is/are rejected.

7) Claim(s) 19-22 is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

| | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ . |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ . |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 16-18, 28-30, 32-33 are rejected under 35 U.S.C. 102(b) as being anticipated by Kwon et al. U.S. Patent No. 6,084,259.

Kwon anticipates a method of forming and the device of a CMOS image sensor, col. 2, lines 41-42, the method comprising: in fig. 7, forming a substrate 501 with a first conductive type of a semiconductor material, P, and forming a layer 503 on top of the substrate, the layer being a second conductive type, N, so as to form a junction that prevents substrate noise diffused into photo elements formed above the layer when a first voltage is applied to the substrate and a second voltage is applied to the layer, in figs. 3-4, col. 3, lines 64 - col. 4, lines 5, wherein the junction is reversely biased, col. 4, lines 15-31, in fig. 7, and wherein the CMOS image sensor is integrated with accessory CMOS circuits 640, 650, fig. 6, to facilitate the CMOS image sensor to operate as desired, col. 6, lines 39-57;

limitations from claims 17, 29, the method wherein the junction is so formed to be a substrate noise barrier in the CMOS image sensor, col. 2, lines 38-57;

limitations from claims 18, 30, the method further comprising: in fig. 7, forming a deep well of the second conductive type 503 in the layer to prevent latch-up between the accessory CMOS circuits and the substrate;

limitations from claims 27, 33, the method, wherein the substrate is of P type, and the layer is of N type; and wherein the first voltage is lower than the second voltage;

limitations from claim 32, in fig. 7, the CMOS image sensor wherein a deep well 630 of P type is formed in the layer to prevent latch-up between wells to form a photo element and the substrate;

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 23-27, 31, 34, 35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kwon as applied to claims 16-18, 28-30, 32-33 above, and further in view of Nakashiba, U.S. Patent No. 6,472,698 B1.

For claim 26, 31, Kwon allows for routine modifications by practitioners, col. 7, lines 52-57, which includes the well-known practice of inverting the polarity of the substrate, its subsequent layers, and their commensurate voltages.

Kwon does not specify planarizing a top oxide, but Nakashiba does. Nakashiba teaches limitations from claims 23, 25, in fig. 4B, wherein the said first conductive type semiconductor is P type substrate 1, the said second conductive type semiconductor material layer is a N- type epitaxial layer, 4, a well-known practice to invert the charging types for device manufacturing, col. 1, line 61 - col. 2, line 14; growing a top oxide layer, referred to as 34 and 44, and after growing a top oxide layer, carrying out a process of chemical mechanical polishing (CMP), col. 4, lines 1-15; wherein a distance between a microlens and a photo element in the CMOS image sensor is reduced by adding an extra process of chemical mechanical polishing (CMP) to eliminate a conventional planarization layer such that sensitivity of the photo element is increased. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to recognize that combining Nakashiba's process with Kwon's invention would have been beneficial because it has the correct height and flat surface to form microlens 35.

For claims 24, 34 Kwon does not specify the depth of the deep wells for preventing latch-up, but the practitioner may optimize these parameters.

"Normally, it is to be expected that a change in temperature, or in concentration, or in both, would be an unpatentable modification. Under some circumstances, however, changes such as these may impart patentability to a process if the particular

ranges claimed produce a new and unexpected result which is different in kind and not merely degree from the results of the prior art . . . such ranges are termed 'critical ranges' and the applicant has the burden of proving such criticality . . . More particularly, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation."

In re Aller 105 USPQ 233, 255 (CCPA 1955). See also In re Waite 77 USPQ 586 (CCPA 1948); In re Scherl 70 USPQ 204 (CCPA 1946); In re Irmscher 66 USPQ 314 (CCPA 1945); In re Norman 66 USPQ 308 (CCPA 1945); In re Swenson 56 USPQ 372 (CCPA 1942); In re Sola 25 USPQ 433 (CCPA 1935); In re Dreyfus 24 USPQ 52 (CCPA 1934).

Note that the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising there from. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Claim Objections

Claims 19-22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 571-272-1854. The examiner can normally be reached on Full Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2823

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



30 August 2004
WB